

15/072103  
02/08/2002

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PATENT NUMBER and  
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM	FILING DATE	CLASS	SUBCLASS	GAU	EXAMINER
10072103	02/08/2002	350	38	3504	J. CHO

\*\*APPLICANTS: Verdoorn David; Woodward Sandra;

\*\*CONTINUING DATA VERIFIED:

NONE for

\*\* FOREIGN APPLICATIONS VERIFIED:

NONE for

PG-PUB	DO NOT PUBLISH <input type="checkbox"/>	RESCIND <input type="checkbox"/>
Foreign priority claimed <input type="checkbox"/> yes <input checked="" type="checkbox"/> no		ATTORNEY DOCKET NO
35 USC 119 conditions met <input type="checkbox"/> yes <input checked="" type="checkbox"/> no		ROC920010224US1
Verified and Acknowledged Examiners's initials		
TITLE : Method and apparatus for debugging a chip		

U.S. DEPT. OF COMM /PAT & TM-PTO-436L (Rev. 12-94)

<b>NOTICE OF ALLOWANCE MAILED</b>		<b>CLAIMS ALLOWED</b>	
		Total Claims	First Claim for
		1	0.0
<b>ISSUE FEE</b>		<b>DRAWINGS</b>	
Amount Due	Date Paid	Sheets Drawn	Pages Drawn
<input type="checkbox"/> <b>TERMINAL</b>		<b>Application Examiner</b>	
<b>DISCLAIMER</b>		<b>PREPARED FOR ISSUE</b>	
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